

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

BONG-HWA JEONG

Art Group:

Application No.:

Examiner:

Filed:

For: **Semiconductor Memory Device Reducing
Noise**

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: 7/19/03

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<p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p><i>(use as many sheets as necessary)</i></p>				Complete if Known	
				Application Number	
				Filing Date	
				First Named Inventor	Bong-Hwa Jeong
				Art Unit	
				Examiner Name	
				Attorney Docket Number	51876P357
Sheet		of			

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (05-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 05/02/2003.

Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Information Disclosure Statement

New U.S. Patent Application for
SEMICONDUCTOR MEMORY DEVICE REDUCING NOISE
Our Ref. No.: P02H6022/US/yh

Reference No.:

- (1) JP Laid-Open No. 09-219091
- (2) US Patent No. 6,246,620
- (3) US Patent No. 6,459,964
- (4) US Patent No. 6,088,291
- (5) US Publication No. 2001/0021140 A1